

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		((((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3)) and test\$3 and (memory with counter) US "3832535" A	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR OR	ON OFF	2005/09/04 23:53 2003/06/23 08:54
L2	21	((((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3)) and (memory same counter) and 714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 06:30
L3	27	((((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3)) and (memory same counter) and 714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 06:30
L4	2	((((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3)) and (memory same counter) and ((ris\$3 and fall\$3) near (compar\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 06:31
S1	50	(word near generator) with (memory RAM ROM EPROM EEPROM CAN cache FLASH) with (counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 10:19
S2	15	((word near generator) with (memory RAM ROM EPROM EEPROM CAN cache FLASH) with (counter) ) and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 13:41
S3	14646	((word pattern data bit bits digital) near generator).ti.((word pattern data bit bits digital) near generator).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 13:49
S4	14646	((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:19

S5	692	((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.) and ((memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM) with (counter "address counter"))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 14:12
S6	42	(((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.) and ((memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM) with (counter "address counter")))) and ((rise fall edge) near (time clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:41
S7	46	(((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.) and ((memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM) with (counter "address counter")))) and ((ris\$3 fall\$3 edge) near (time clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 14:16
S8	31	((((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.) and ((memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM) with (counter "address counter")))) and ((ris\$3 fall\$3 edge) near (time clock))) and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:28
S9	31	((((((word pattern data bit bits digital) near generator).ti. ((word pattern data bit bits digital) near generator).ab.) and ((memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM) with (counter "address counter")))) and ((ris\$3 fall\$3 edge) near (time clock))) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (latch flipflop flip-flop flip flop compar\$4 word generator memory RAM ROM EPROM EEPROM CAN cache FLASH counter ris\$3 fall\$3 edge tim\$3 clock\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 14:24
S10	22344	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:59

S11	180	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and 714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:27
S12	7	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and 714/730.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:27
S13	16	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and 714/728.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:28
S14	4340	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:00
S15	3296	((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:00
S16	50	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and 714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:52
S17	4	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and 714/730.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:29
S18	9	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and 714/728.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:30

S19	61379	Chen.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:48
S20	126287	Chen, Hsin-Chu.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:49
S21	0	("Hsin-Chu Angus Chen").in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:49
S22	1	("Hsin-Chu Chen").in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:50
S23	0	("Hsinchu Chen").in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:50
S24	0	(Chen).in. and (Hsinchu).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:51
S25	1	(Chen).in. and (Hsin-chu).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 15:51
S26	563	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and ((rise fall edge) near (time clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:00
S27	9818	(((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:04

S28	516	Chen.in. and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:06
S29	20731	(Chen, Hsin-Chu.in.) and (count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:06
S30	0	(("Hsin-Chu Angus Chen").in.) and ((rise fall edge) near (time clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:06
S31	492	(((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.) and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:05
S32	492	((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and ((latch flipflop flip-flop "flip flop") and compar\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:06
S33	324	((((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:06

S34	42	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and ((rise fall edge) near (time clock))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:07
S35	49	(((((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ti. (((word pattern data bit bits signal digital) near generator) with (stor\$3 memory ROM RAM EPROM EEPROM cam cache FLASH DRAM SRAM SROM)).ab.) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and ((latch flipflop flip-flop "flip flop") and compar\$4)) and (count\$3)) and ((ris\$3 fall\$3 edge) near (tim\$3 clock\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/18 16:10
S36	1	"5390129".PN.	USPAT	OR	OFF	2003/06/18 16:17
S37	0	(word near generator) near ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 10:26
S38	2	(word near generator) and ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) near (stor\$3 and (test near (data signal bits bit information word))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 11:21
S39	35	(word near generator) and ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 11:02
S40	10	(word near generator) same ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 11:21

S41	0	((word near generator) same ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))) not ((word near generator) and ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 11:21
S42	10	((word near generator) same ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))) and ((word near generator) and ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word))))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 11:21
S43	5	(word near generator) with ((memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM) with (stor\$3 and (test near (data signal bits bit information word)))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 14:32
S44	2178	kobashi.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:34
S45	0	kobashi.in. and (iterative adj decoding)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:35
S46	0	kobashi.in. and (iterative near decoding)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:35
S47	0	kobashi.in. and (iterative near decod\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:35
S48	0	kobashi.in. and (iterative)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:35

S49	0	kobayashi.in. and (iterative)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:35
S50	0	kobashi.in. and (iterative)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:36
S51	22	kobayashi.in. and (iterative)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:36
S52	2	kobayashi.in. and (iterative adj decoding)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 13:36
S53	3115	(stor\$3) near (test near (word pattern data bit bits signal signals patterns words))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 14:55
S54	193	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words))).ti.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 14:57
S55	1190	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words))).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 14:59
S56	0	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words))).ab. and ((word near generator) same (memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 15:24
S57	1	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words))).ti. and ((word near generator) same (memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 15:04

S58	11	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words)))". and ((word near generator) same (memory RAM ROM EPROM EEPROM CAN cache FLASH DRAM SDRAM))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/06/19 15:27
S59	58	"digital word generator"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:03
S60	4	"3832535".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:03
S61	2	"4733395".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:06
S62	3749333	EP "798535".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:06
S63	1	"798535".pn.	EPO	OR	OFF	2003/06/19 16:07
S64	2	"5818379".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:08
S65	2	"20020073368".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:09
S66	2	"20020073368"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:09
S67	26	((stor\$3) near (test near (word pattern data bit bits signal signals patterns words))) and 714/744.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/06/19 16:10
S68	921569	(word data pattern sequence signal) near (generat\$3 comput\$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:40

S69	813374	(word data pattern sequence signal) near (generat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:47
S70	692252	(word data pattern sequence signal) adj (generat\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:40
S71	44812	((word data pattern sequence signal) near (generat\$3)) and (memory same counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:51
S72	7518	((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3) and (memory same counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 06:30
S73	16516	((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3) and test\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:52
S74	1252	((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3) and test\$3).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:52
S75	2127	((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3) and test\$3).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/04 23:52
S76	1772	((word data pattern sequence signal) near (generat\$3)) with (pre\$determin\$3 pre\$set pre\$stor\$3) and test\$3 and (memory with counter)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/09/05 00:02
S77	227	714/744.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 01:57
S78	3134	714/724.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 06:29

S79	114	714/728.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 02:28
S80	1235	714/738.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/09/05 02:28